FIG. 1 (PRIOR ART)

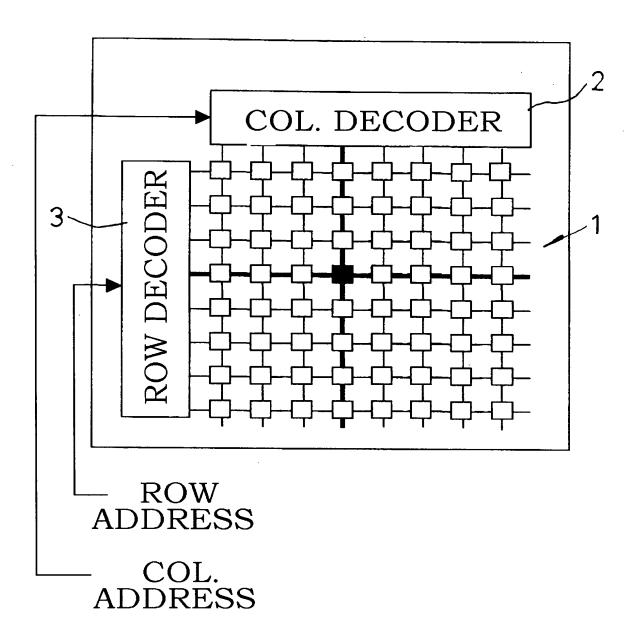


FIG. 2

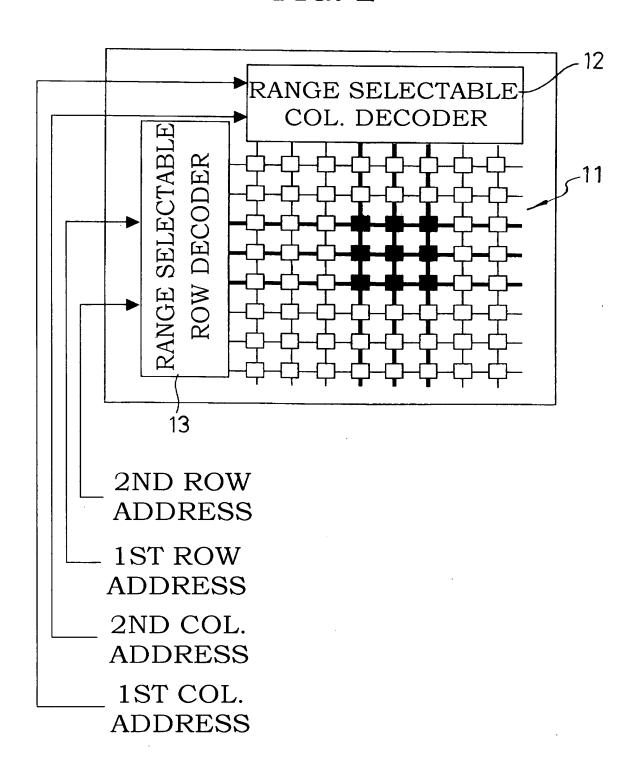


FIG. 3

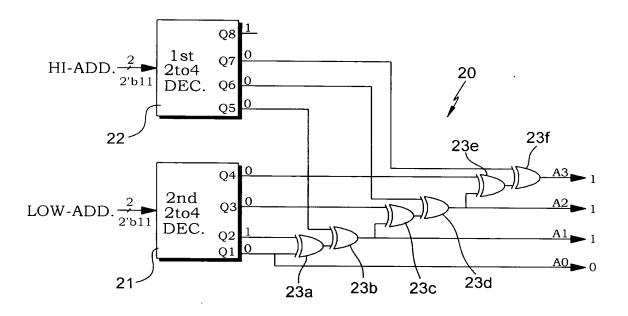


FIG. 4

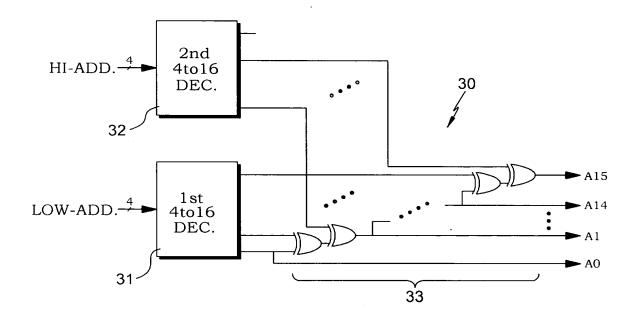


FIG. 5

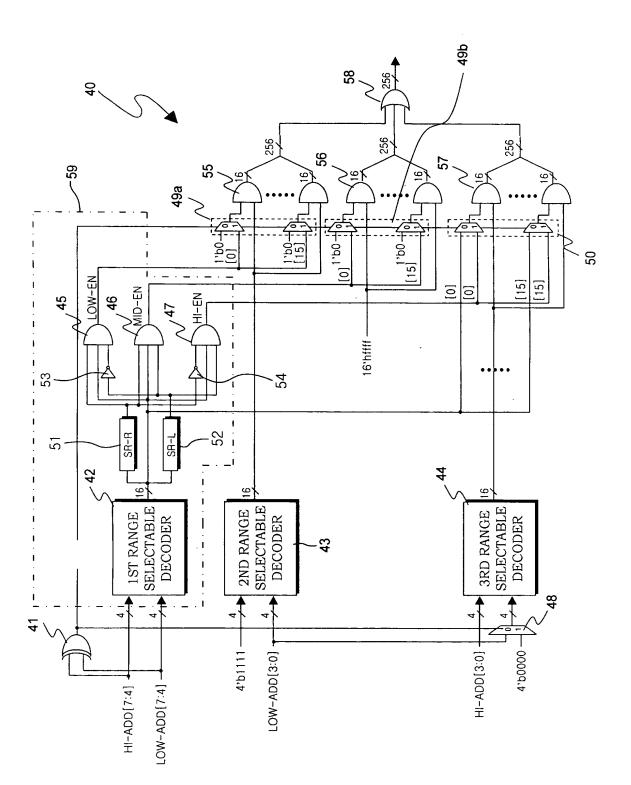


FIG. 6

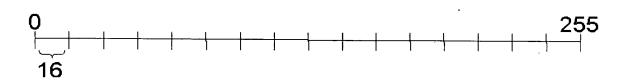


FIG. 7

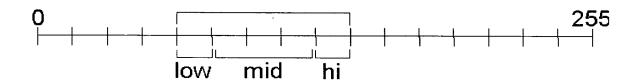


FIG. 8

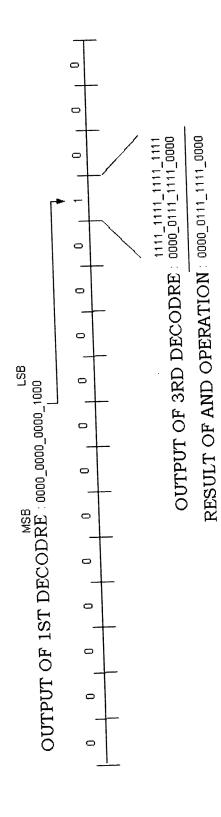


FIG. 9A

1ST DECODER: 000000000111 1 000 Shift Right: 000000000011 1 1000 ~Shift Left: 11111111110000 1 1111

FIG. 9B

1ST DECODER Shift Right ~Shift Left	: :	000000000 0000000001 0000000011	11 11 11	1000 11000 0000
MID-EN	•	000000000	11	0000

1

FIG. 9C

1ST DECODER: 000000000 | 1 | 111000

Shift Right: 1111111111 | 1 | 0000111

~Shift Left: 000000001 | 1 | 110000

HI-EN: 000000000 1 000000

FIG. 10 OUTPUT OF 2ND RANGE 1111_1111_1111 SELECTABLE DECODER: 1111_1111_0000_0000 RESULT OF AND OPERATION: 1111_111_0000_0000 0 0 low SECTION 56 1111_1111_1111_1111 ALL'S ONE: 1111_1111_1111 RESULT - 1111_1111_1111_1111 OF AND : 1111_11111_1111 65 98 86 mid SECTION 0 OUTPUT OF 1ST RANGE: 0000_0000_0111_1000 SELECTABLE DECODER 0 1111_1111_1111_1111 RESULT OF AND OPERATION: 0000_0000_0000 0 MSB 0 0 0 0 hi SECTION 0 0 255

FIG. 11A

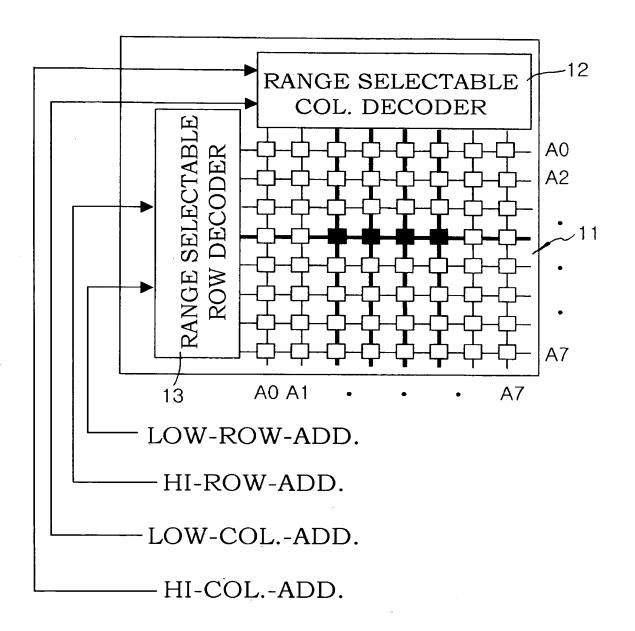


FIG. 11B

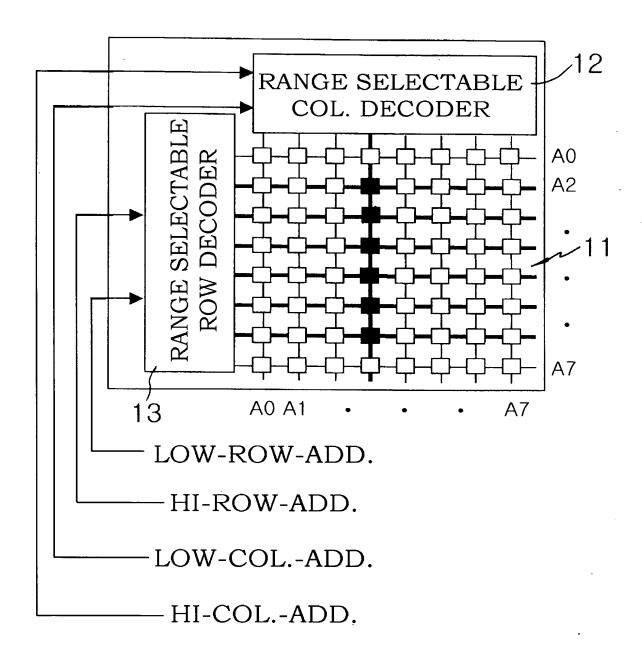


FIG. 11C

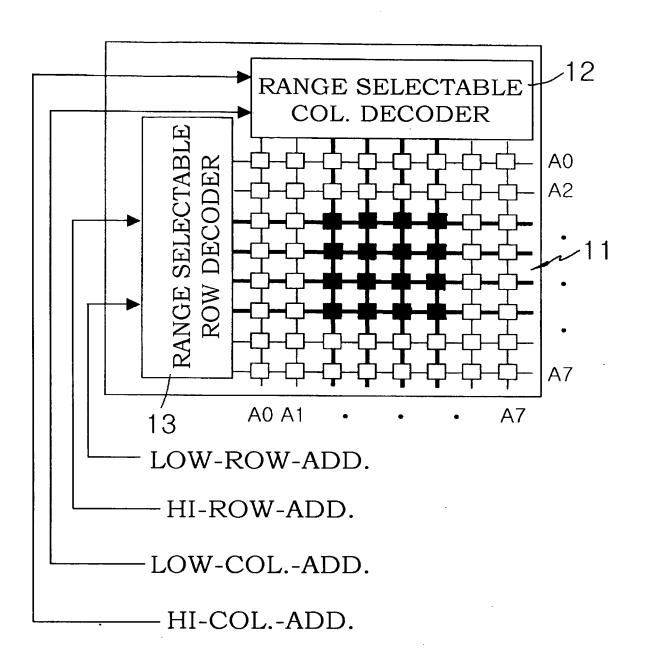


FIG. 12

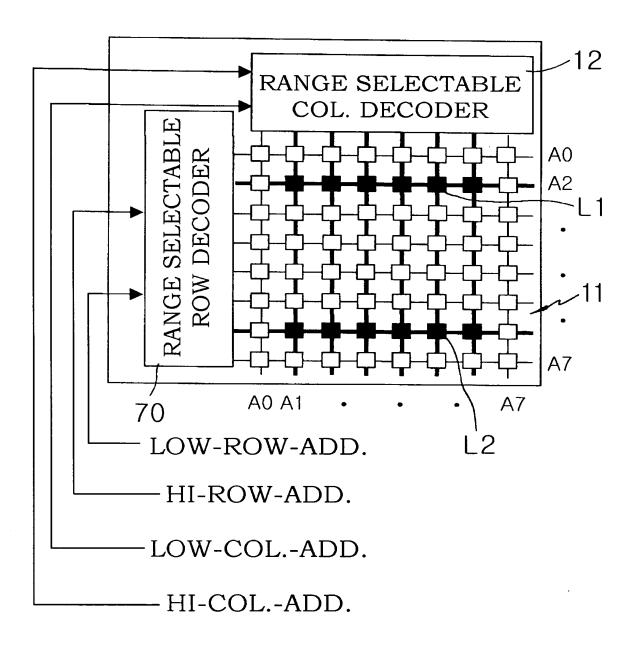


FIG. 13

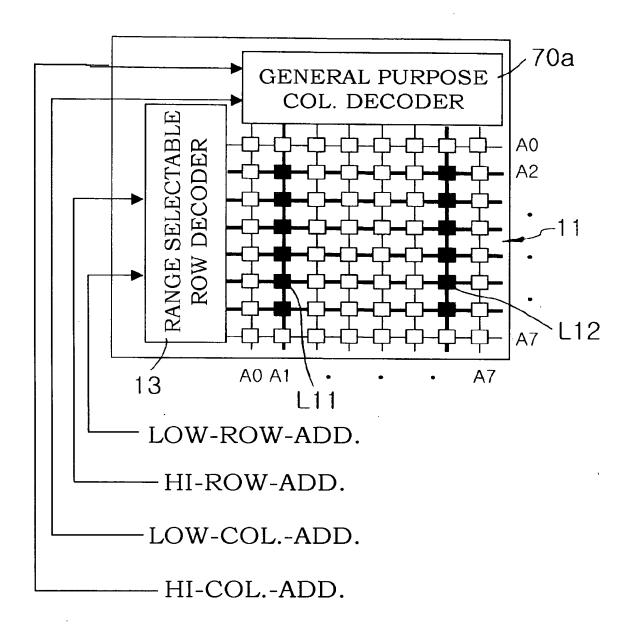


FIG. 14

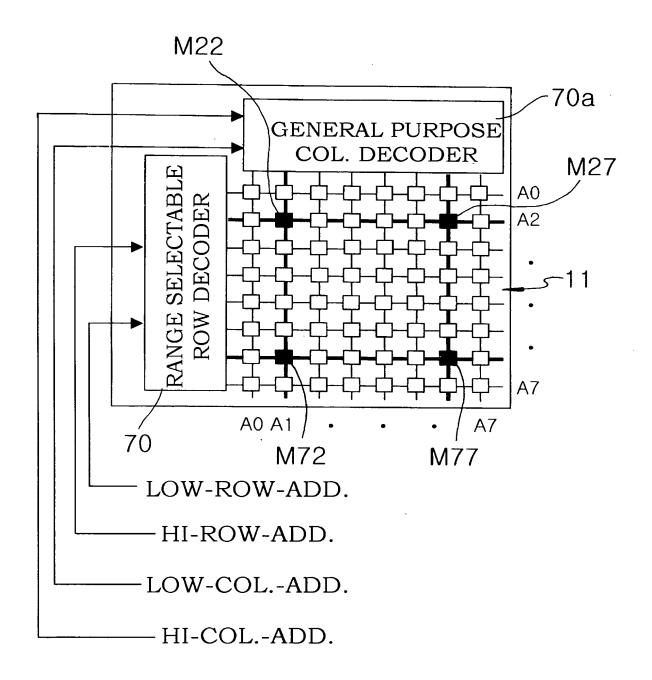


FIG. 15

